Hazem El Tahawy

Education

1976	Graduated from Faculty of Engineering – Communications & Electronics Department- Cairo University
1982	Master's degree, from Polytechnic Institute – Grenoble, France – in the field of Electronic Design
1987	Ph.D. Degree, from Polytechnic Institute – Grenoble, France – in the field of Electronic Design Automation

Positions Occupied

Mar 2021- Present

VP Siemens EDA (Siemens Industry Software) MENA region.
- 3 main development sites in Egypt, Morocco, and Tunisia
- Total number of employees in the 3 sites around 700

In 2017 Siemens acquired Mentor Graphics

Jan 2011- Mar 2021

Mentor Graphics Managing Director MENA region
- Establishing Mentor Graphics Tunisia Site (Today 35 Engineers)
- Establishing Mentor Graphics Morocco site (today 25 Engineers)
- Continue growing Mentor Graphics Egypt site (today 640 Engineers)

1998-2011

Managing Director of Mentor Graphic Egypt / Arab Area Headquarters supporting different development and sales activities in Egypt:
- Increase Number of Engineers from 27 to 280
- Increase Operation Budget from $675K to $12 M
- Increase Business contribution for Mentor Graphics from $1M Up to $45M based on the operation development

1994-1998

Analog-Mixed Signal (AM/S) Engineering Director Worldwide & overall   management of Anacad® S.A.R.L (France)

In 1994, Mentor Graphics Corporation acquired Anacad

1991-1994

ANACAD General Manager
- Software Development, IC Design, Sales & Marketing

- Volume in Business in 1991: $ 2.5 M
- Volume in Business in 1994: $6 M (50% of all ANACAD Corporate revenue)

Co-Founder Anacad® S.A.R.L (France), Specialized in Software Development & Integrated Circuits (ICs) Design (first EDA Startup for Analog Design in France)

Head of Computer Aided Design (CAD) Department

Professional experience

1978-1989

Cairo University, Egypt
- Instructor in Computer Science Department

1982-1987

Grenoble National Center of Telecommunication (CNET), part of France Telecom, France
- Research Engineer
- Working in my thesis for developing new Methodology for Analog Design Verification and Simulation
- Driving research in the Electronics Design for Telecommunication Application (mainly Analog and Mixed Circuits)

1987-1991

Anacad SARL, Grenoble, France
- CAD Department Manager
- Managing all engineering activities for design and CAD
- Responsible on the productivity of the Simulation Tool as well as Synthesis tools for Analog Design Circuits
- Managing all customers' needs and requirements (STM, Philips, Thomson, Dassault…)

1991-1994

- Anacad SARL, Grenoble, France
- General Manager
- Overall management: Software Development, Electronics Design, Marketing and Sales)
- Increase Business volume from $2.5 - $6 M
- Expand Company branches to Belgium, and USA

1994-1997

USA/France Mentor Graphics Engineering director
- Responsible of WW AMS Development in Mentor Graphics

- Managing all the different Development sites (USA, France, Germany, and Egypt)
- Support all International Customers for AMS Products

1998-2011

Egypt, Mentor Graphics Managing Director
- Overall Management of the Egypt Site
- Increase Engineering Headcount from 27 to 220
- Increase Operation budget from $675 to $8.8M
- Increase Sales Contribution from $1M to $35M

2011-2021

MENA region, Mentor Graphics Managing Director
- Establishing sites in Morocco and Tunisia
- Continue growing Egypt site
- Total employees achieved around 700
- Publishing 400 Technical papers and 40 Patents
- Empowering Cooperation with Universities in the MENA region using HEP and CBP programs, today we have about 120 logos
- Building Partnership with key organization in the region such as ITIDA (Egypt), KACST(Saudi Arabia), ATIC & GF (UAE), MMC (Morocco), IPark (Jordan), Qatar Foundation(Qatar), ASTF (UAE)
- Contribution in organization and sponsoring key technology IEEE conferences in the region such as IDT, ICN, ICECS,

Language skills

- Arabic (Excellent in Reading, Speaking, and writing)
- English (Excellent in reading, very good in speaking and writing)
- French (Excellent in reading, very good in speaking and writing)

Other qualifications	• Membership of professional bodies: IEEE (Circuit & System, Computer Society, Design and Test, Embedded System, Management and Quality Control), EITESAL, AmCham, CIT, SEBA, SECC, Engineering Chamber (Electronics section)
	• Member of the Team Manage the Implementation of Suez Factory for Cement in 1978 Project Management Flow based on Pert Methodology
	• Member of the IEEE Standardization Team for Behavioral Description Language based on the DoD Proposal (VHDL), 1982-1985
	• Member of the IEEE Standardization Team for Analog Mixed Extension of VHDL, VASASG (VHDL Analog SubPAR Analysis and Standardization Group) 1985-1994
	• Participate in different European Projects in Electronics Design and Verification such as ESPRIT, ACTS and SMILE
	• Participate in DoD Tender for Standardization of AMS Extension of VHDL with International Electronics Firms and Universities
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Area Of Expertise	• Electronics Design (Analog/Digital Design and Verification) • Embedded System Design (System Level Design and Application) • Software Development (Software Architecture and Development Frame) • Software Quality & Test (Methodology for Test and Quality for Software and Embedded System) • Multinational Culture in Management and Strategy Development
Publications	
	- 5th IEEE EAST-WEST DESIGN & TEST INTERNATIONAL SYMPOSIUM, Invited Presentation on" System On Chip (SOC) Design Pressures reach Critical Points", Congress Hotel, Yerevan, Armenia, September 7-10, 2007.
	- IEEE DESIGN & TEST Magazine," Electronics Design Industry in the Middle East", March –April 2007, Vol.24 No.2, pp204-206
	- IEEE International Workshop for System On Chip (IWSOC), "System On Chip Design reaches Critical points", Keynote

Speech, November 2006, Cairo, Egypt

- IEEE International Design & Test Workshop, Panel Chairman for " Electronics Industry in the Middle East: Opportunities & Challenge", October 2006, Dubai, UAE.

- "Towards a good Partnership for Electronics Industry Development", Invited Paper, Arab Workshop organized by the Arabic Association for Mining & Industrial Development, 23rd of May 2005, Tunisia

- 16th IEEE International Conference in Microelectronics, ICM 2005, Invited Paper on "The Small and Medium Enterprises Support Center (SMESC)", 6-8 December 2004, Tunisia.

- IEEE Engineering Day " EDA Industry Growth- Are There Enough Problems to Solve", 8th of August 2005, Cairo Egypt.

- IEEE International Conference for Electrical, Electronics and Computer, Invited Paper, "Problems for Soc Design", 5-7 September 2004, Ain Shams University, Cairo Egypt.

- 15th IEEE International Conference in Microelectronics, ICM 2003, Invited Paper on "Technology Trends and Its Impact on Design Tools", Invited Presentation, October 2003, Egypt.

- "Electronics Design Trends & Opportunities", Invited Paper, October 2002, Dubai, UAE, Arab Workshop Organized by the Arabic Association for Mining & Industrial Development.

- 14th IEEE International Conference in Microelectronics, ICM 2002, Invited Presentation on "Advanced Solution for System On Chip Design", Invited Paper, October 2002, Lebanon.

- First International Internet-Education Conference, Invited Paper "E-Learning in Electronics Design", October 2002, Cairo, Egypt

- 13th IEEE INTERNATIONAL Conference in Microelectronics, ICM 2001, Invited Paper on" E-Commerce New Trends: Virtual Design Support Center (VDSC)", October 29-31, 2001, Rabat, Morocco.

- 10th IEEE INTERNATIONAL Conference in Microelectronics, ICM 1998, Invited Paper on" Integrated System Design driving the Next Decade of Design", 14-16 December 1998, Monastir, Tunisia.

- IEEE European Design Automation Conference, "VHDELDO: A

New Mixed Mode Simulation", 20-24 September 1993.

-   IEEE International Symposium on Circuits and Systems (ISCAS 89), " Functional Verification of Analog Blokes in FIDELDO: A unified Mixed Mode Simulation Environment", 8-11 May 1989.

-   "A New Implementation Technique for the Simulation of Mixed (Digital-Analog) VLSI Circuits", Proc. IEEE Int. Conf. on Computer Aided Design (ICCAD'87), 1987, CA, USA.

-   "FIDEL: A multilevel Hardware Description and Simulation Language", Proc., Integrated Circuit Technology Conf., Limerick, Ireland, Sept. 1986.

-   "Integration of Functional Model in Electrical Simulator", Proc. IEEE Int. Symp. Circuits & Systems (ISCAS'86), 1986, Oregon, USA

-   "Integration of Functional Model in Logic Simulation", Proc. International Symp. ICS'84, Taipei, 1984.

-   "Functional Modelling for Logic Simulation", IEEE International conference in Computer Design, ICCD, 1984, New York, USA